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24109 7590 07/20/2004

XILINX, INC
ATTN: LEGAL DEPARTMENT
2100 LOGIC DR
SAN JOSE, CA 95124

08/30/2004 GWORDF2 00000017 240040 10693218

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Pat. Slaback (Depositor's name)
Pat. Slaback (Signature)
August 27, 2004 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10693,218	10/24/2003	Tao Pi	X-1128-ID US	2270

TITLE OF INVENTION: FPGA LOOKUP TABLE WITH TRANSMISSION GATE STRUCTURE FOR RELIABLE LOW-VOLTAGE OPERATION

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	10/20/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
LE, DON P	2819	326-041000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- ☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
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2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

XILINX, INC.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

San Jose, California 95124

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

- ☒ Issue Fee
☐ Publication Fee (No small entity discount permitted)
☐ Advance Order - # of Copies _____

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is not claiming SMALL ENTITY status. See, e.g., 37 CFR 1.27(g)(2).

The Director of the USPTO is requested to apply the Issue Fee and Publication Fee (if any) or to re-apply any previously paid issue fee to the application identified above.

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(Authorized Signature)

(Date)

Lois D. Cartier 40,941 8/27/04

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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COMMISSIONER FOR PATENTS

P. O. Box 1450

Alexandria, Virginia

22313-1450



Inventor(s): Tao Pi et al.

Assignee: XILINX, INC.

Serial No.: 10/693,218

conf.no. 2270

Filed: October 24, 2003

Title: FPGA Lookup Table with Transmission Gate Structure for Reliable Low-Voltage Operation

Docket No.: X-1128-1D US

Enclosed: Return Receipt Postcard

Fee Transmittal - Part B

Date: August 27, 2004

VIA FACSIMILE 703-746-4000

Agcnt/Sec: LDC/pbs

LDC